

## Professional Résumé

**Enrique Mayorca**

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**OBJECTIVE:**

Seeking entry-level employment or internship in the electrical engineering field.

**EDUCATION:**

**California State University, Long Beach** (August 2011 - December 2016) Cumulative GPA: 3.496/4.0  
 Bachelor of Science in Electrical Engineering Graduation Date: Dec 2016

**INTERNSHIP EXPERIENCE**

**Los Angeles Unified School District – Central Region Project Execution:** *Admin. Intern. 1 April 2015-February 2016*

Coordinated weekly project meetings alongside contractor, architect, and engineer in the construction of a low voltage audio and visual communication system as an Assistant Project Manager.

Assisted Owner-Authorized Representative in managing a \$323,250.00 project involving the installation of audio and visual communication systems.

**PROJECTS/RESEARCH**

**DC to AC Inverter using Step Up Transformer** *May 2016-July 2016*

Researched various DC to AC Inverter circuit designs and electronic components such as 555 timers and transistors.

Tested and assembled prototypes of DC to AC inverters using digital multimeter and breadboard.

**Solar Panel Charger, CSULB** *February 2016-May 2016*

Researched solar charger circuit designs and electronic components to construct a functional solar charger prototype.

Implemented a solar charger circuit design and assembled a prototype on a breadboard.

Developed testing procedure for solar charger prototype using DC power supply, digital multi-meter, and 6v batteries.

**NASA Rascal-Ops, CSULB** *August 2014-May 2015*

Collaborated with student engineers to produce wiring schematics for two lunar rovers.

Researched tire tread, suitable H-bridge, battery banks, motors, and relay switch circuits for lunar rovers.

Assisted colleagues in creating arenas simulating rocky fields, surface craters and sand fields for testing purposes.

**Proportional-Integral-Derivative Temperature Controller, CSULB** *September 2014-December 2014*

Wired an incubator with temperature controller circuit on a Basic Stamp Homework Board.

Used bias drive-to-drive incubator temperature to a desired set point and simulated circuit on Stamp Plot.

Readjusted Proportional-Derivative-Integral constants to eliminate errors caused by disturbances such as a small DC fan.

**Digital System Design, CSULB** *February 2014-May 2014*

Coded a 2 to 4 decoder, 3 to 8 decoder, digital lock, universal shift register and simple calculator in VHDL.

Created test bench for each project for simulation purposes prior to implementing and uploading code to FPGA board.

Familiarized with instantiation of entities and Algorithmic State Machines for coding simplification.

**LEADERSHIP/ STUDENT ORGANIZATIONS**

**Society of Hispanic Professional Engineers (SHPE), CSULB:** *Hospitality Chair February 2013-April 2015*

Provided professional, educational and cultural support to the community and incoming students.

Mentored freshmen to develop their professional and leadership skills.

**SOFTWARE & LAB EQUIPMENT EXPERIENCE**

**MATLAB:** Used MATLAB to display the magnitude and phase spectra of filters, sampling and interpolation of signals under Nyquist Rate conditions, linear time invariant systems and the spectrum of their DTFT equivalent.

**AutoCAD:** Familiarized with osnap, xref, isometric, orthographic, reference lines terminology and drafting commands.

**OTHER SOFTWARE & HARDWARE SKILLS:** VHDL, FPGA, Xilinx, Assembly Language, Arduino Uno, PCB,

MS Office (Excel, Word, PowerPoint), OrCad Cadence, JAVA, Soldering, Fritzing, Eagle CAD, AutoCAD,

SolidWorks, Welding. **Languages:** Fluent in Spanish; basic Japanese**AWARDS**Edison Scholars Program Scholarship Recipient (CSULB) *May 2015*